FIG. 1

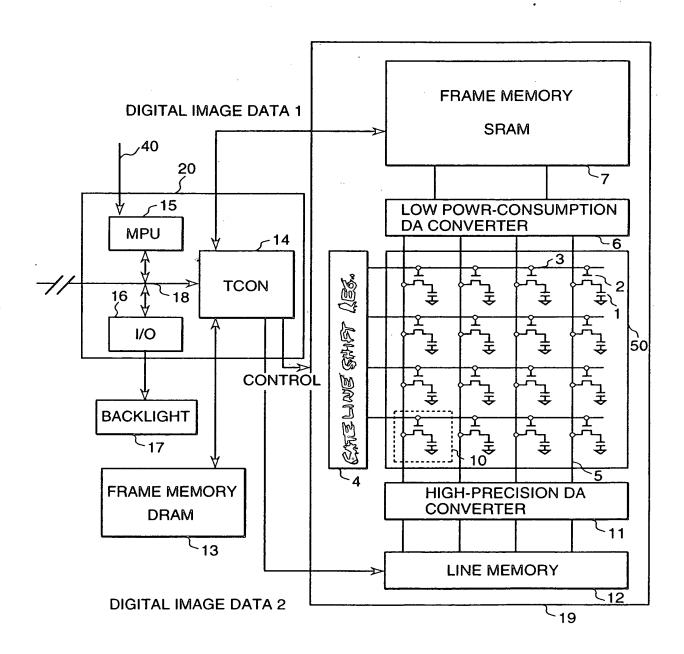
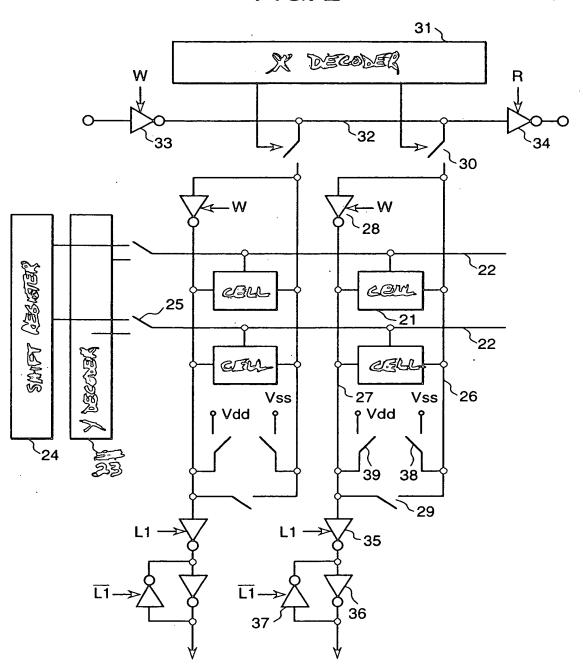


FIG. 2



## FIG. 5(a)

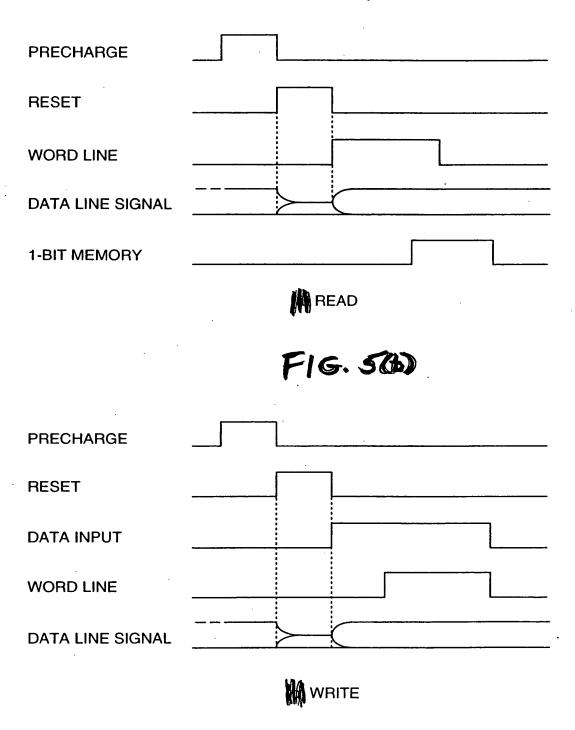


FIG. 6

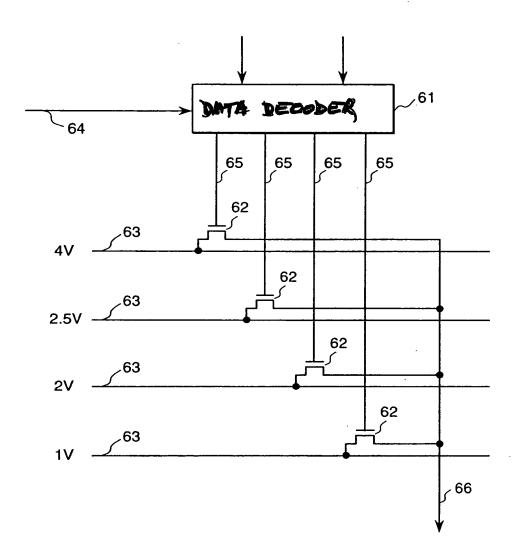


FIG. 8

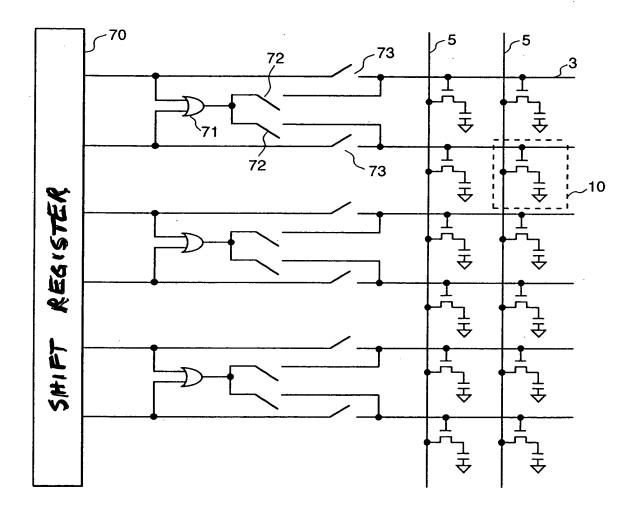


FIG. 10

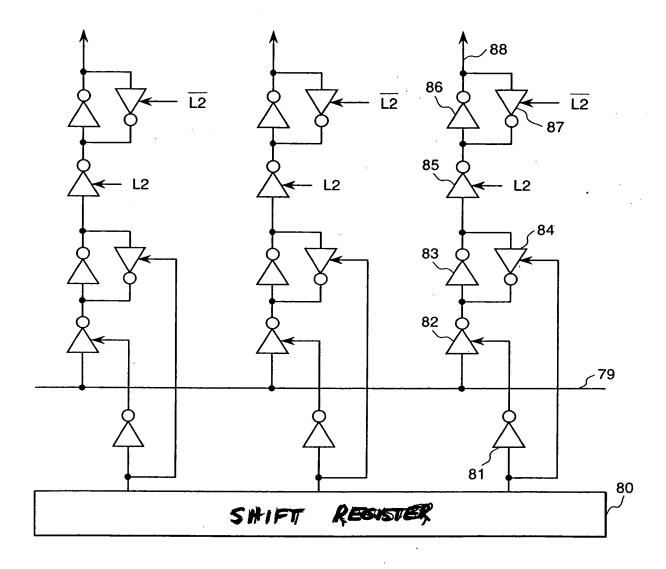
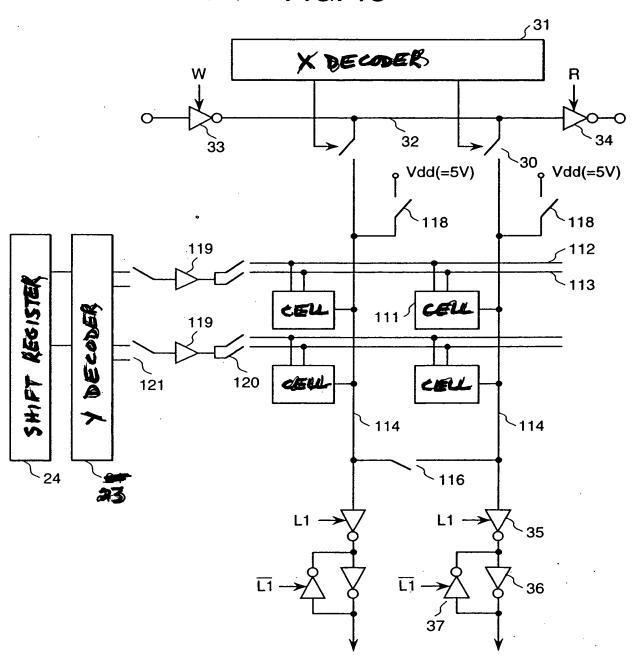


FIG. 13



## FIG. 15(a)

PRECHARGE	
RESET	
WORD LINE	
DATA LINE SIGNAL	
LATCH SWITCH	on
1-BIT MEMORY	
•	READ
	FIG. 150D
PRECHARGE	
RESET	
WORD LINE	
DATA LINE SIGNAL	
LATCH SWITCH	
1-BIT MEMORY	
	414

FIG. 22 164 -163 .17 FRAME MEMORY 18 15 160 PIXEL AREA 16 Ho 14 TCON 161 162 I/F RADIÓ DATA

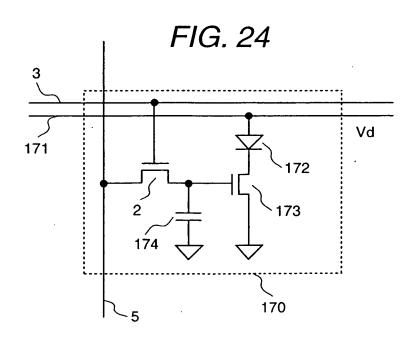


FIG. 23

